

**PROCESSOR TO PROCESSOR COMMUNICATION
IN A DATA DRIVEN ARCHITECTURE**

Abstract

5 In an embodiment, an apparatus includes a first processor that includes a
first processor element. The apparatus also includes a second processor that
includes a second processor element. The first processor is configured to transmit
data to the second processor through a third processor, wherein no processor
element within the third processor is configured to perform a process operation on
10 the data as part of the transmission of the data from the first processor to the second
processor.

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